

Upgrade to PCI Express[®] 2.0 Receiver Test

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Jitter Tolerance Testing

The Agilent J-BERT N4903A high-performance serial BERT offers complete and accurate jitter tolerance testing for receivers. It includes calibrated jitter sources, ISI, SSC and automated jitter tolerance testing. Combined with the Agilent 81150A pulse function arbitrary noise generator, you have the most flexible and powerful receiver tolerance test set up for PCIe[™] 2.0.

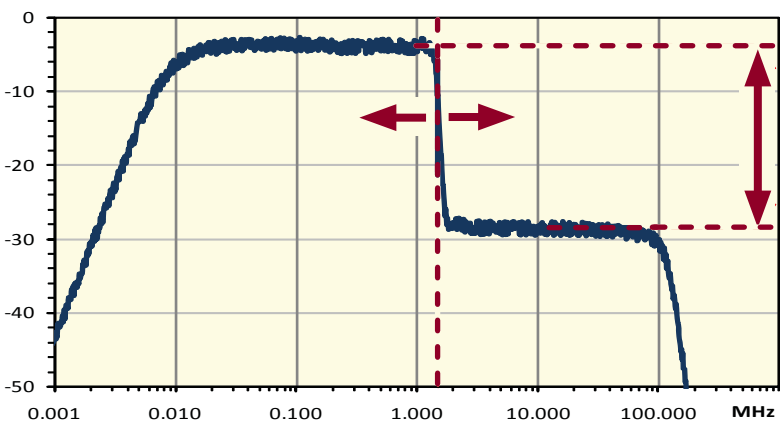


Figure 1. The proper jitter spectrum is achieved by filtering the white noise of the Agilent 81150A with a PCIe 2.0 – specific filter. The filter can serve two topologies: data driven and common clock architecture.

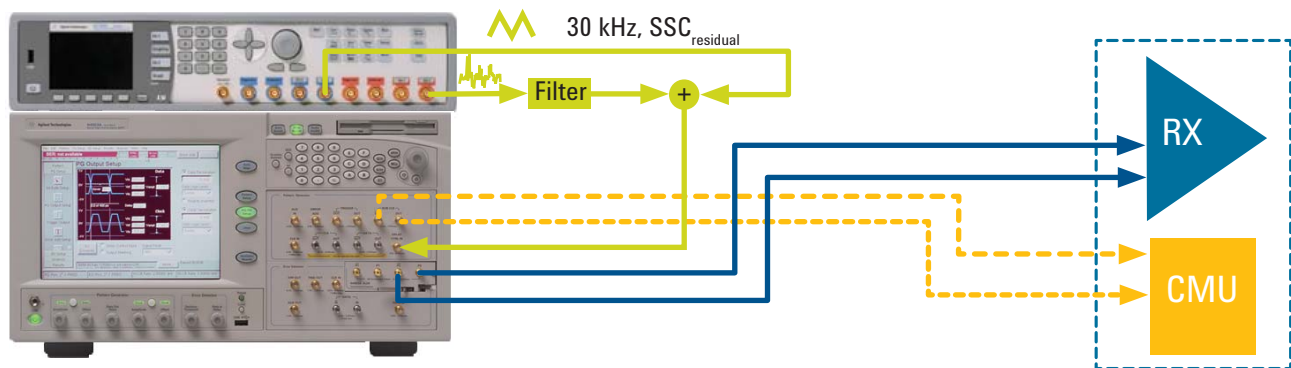


Figure 2. The test setup of a J-BERT with the 81150A: the filtered signal from the 81150A is used as delay control input for the J-BERT.



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Revised: October 6, 2008

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Printed in USA, December 3, 2008
5989-9937EN

Related Literature

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<i>Agilent J-BERT N4903A High-Performance Serial BERT</i>	5989-2899EN
<i>Agilent 15431A Filter Set for 81150A</i>	5989-9826EN
<i>Test Automation Software Platform N5990A</i>	5989-5489EN
<i>Agilent ParBERT 81250, Parallel Bit Error Ratio Tester</i>	5968-9188E
<i>Development Lifecycle from Concept to PCI-SIG® Poster</i>	5989-8762EN

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