



Agilent BestLink/81200

Simulation Data Link for 81200

Data Generator/Analyzer Platform

Product Information

The Agilent Technologies 81200 is an ideal platform with which to verify your digital devices throughout the development cycle under real-world conditions. Its high-speed pattern rate, large pattern depth and scaleable configuration makes it the perfect instrument for your digital devices verification and characterization.

When you want to thoroughly check out a prototype, you usually have to program the 81200 with all the patterns and timing already created and analyzed during simulation. Therefore, once a prototype is available, the whole verification task starts from scratch. All the know-how established during simulation needs to be recreated during prototype verification. This results in a tremendous duplication of efforts. Not only is it costly but also negatively impacts the notorious time to market.

The solution to this problem is straightforward: apply the same stimuli to the prototype already used in simulation and make sure you get the same results.

This is where BestLink/81200 comes in. It provides for an intelligent and highly automated migration of simulation data to the 81200. Once the BestLink/81200 generated patterns are loaded on the 81200 system, prototype debug can begin. Not only are stimulus pattern and timing information retrieved from simulation, but also expected circuit behavior is downloaded to the 81200. This facilitates an easy analysis of the prototype behavior, as now it is possible to compare expected circuit behavior with measured activities within the 81200 environment.

Key Features

- Translates VHDL and Verilog simulation data to the 81200.
- Processes VCD files; generates 81200 conform pattern and cmd files.
- Easily integrates into an EDA verification environment.
- Intelligently adjusts simulation events to 81200 timing resources.
- Checks for simulation data suitability with 81200 requirements.
- Simple set-up by reading users' 81200 HW configuration.
- UNIX-based application; runs on HP 700 and Sun platforms.

Diagonal Systems in Brief

Diagonal Systems develops EDA (Electronic Design Automation) tools for the automation of the verification process as well as in the design and prototype verification areas. Founded in 1987, the company has over 10 years of experience in providing highly automated, comprehensive and easy-to-use verification solutions, both in the design and test areas.

Diagonal Systems is working closely with Agilent Technologies to provide the best in class solution to facilitate an easy migration of simulation data to Agilent's line of prototype verification environments. Diagonal's products are used worldwide by customers in the telecommunications, semiconductor, automation, and aerospace industries. The company is based in Greifensee/Zurich Switzerland.



Agilent Technologies

Innovating the HP Way

Pattern migration process

With BestLink migrating simulation, results are fast and easy. Set-up is minimal. Thereafter a sophisticated algorithm automatically converts your simulation patterns. Now they are ready to be imported into your 81200 environment.

VCD: the standard data format of simulation results.

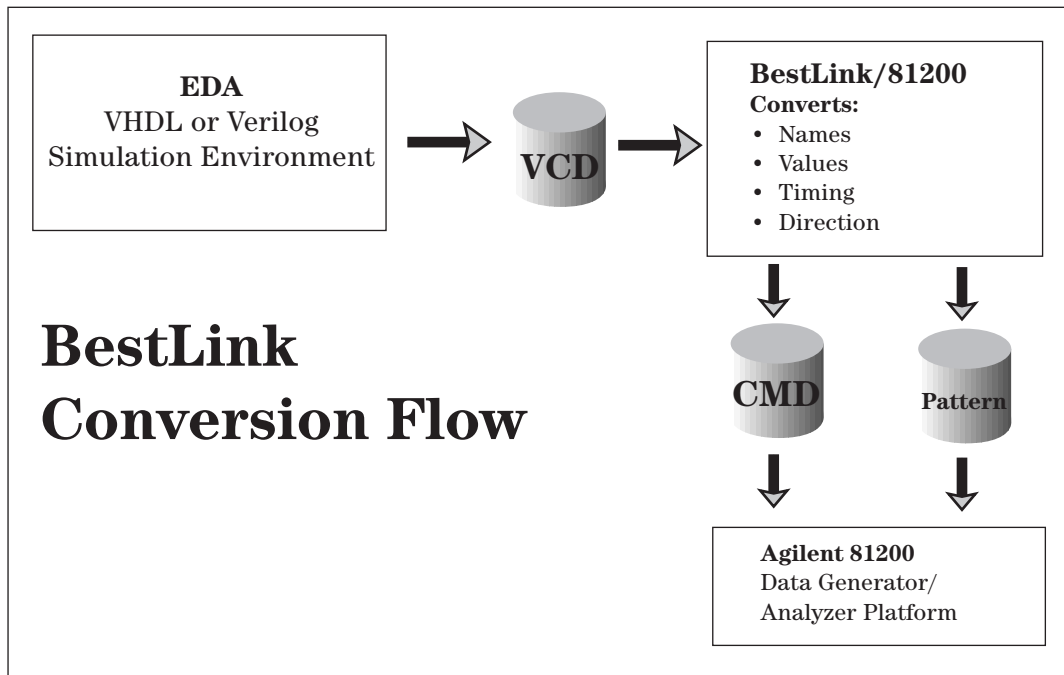
Nowadays most designs are simulated with Verilog or VHDL. The common data format of simulation results available from these simulators is VCD (Value Change Dump).

BestLink/81200 directly imports VCD. No changes or additions need to be made in your simulation environment.

Platform support

BestLink/81200 is available for the following UNIX Platforms:

- HP9000/700 HP-UX9.XX and higher
- Sun Sparc: SunOS and Solaris



Ordering Information

Demonstration and evaluation versions of the software can be downloaded from the Web.

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Related Literature

Agilent 81200 Data Generator/ Analyzer Platform, data sheet
5965-3415E

Agilent 81200 Data Generator/ Analyzer Platform, configuration guide
5965-3417E

Agilent 81200 Data Generator/ Analyzer Platform, brochure
5965-3416E

For more information:
www.agilent.com/find/dvt

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