

Keysight N6705A

DC Power Analyzer

Biasing Multiple Input Voltage Devices in R&D

Application Note

Introduction

This application brief describes using the voltage output synchronization capabilities of modular power supplies in R&D multiple bias applications.

Description

During R&D and design validation stages, some tests begin with multiple, sequenced voltages to power the device under test (DUT). For example, an ATX PC motherboard requires a specific power-on sequence to power the board. It is important to power the board properly to avoid sub-assemblies drawing excessive current. An incorrect power start-up sequence may cause damage to integrated circuits. In turn, this can compromise the reliability and quality of the circuit board. R&D engineers must generate the correct power-up sequence to the board to prevent harming circuits and causing additional problems.

Design validation engineers sequence multiple voltages to evaluate how much margin exists in their designs. They adjust the timing between the power-on voltages until a problem occurs.

Figure 1 describes the input power timing sequence of an ATX motherboard. The ATX PC motherboard requires three power inputs with specific timing requirements before a digital PWR_OK signal is asserted. Once the board is powered, power is distributed to sub-assemblies on the board such as peripheral cards and drives. ASIC, FPGA and DSP test boards have similar power-on testing requirements in R&D and design validation.

Problem

This test requires multiple power outputs for the 12 V, 5 V and 3.3 V lines with precise timing accuracy within the millisecond range. In addition, these power outputs need to have programmable slew rates to simulate the rate of change for the specified timing conditions. Once the right power supplies with the right specifications and features are found, the power supplies are synchronized. Most engineers write programs in various programming environments to sequence the outputs during power on and off. This is a very time consuming task since the engineer needs to find and install drivers, write code, debug their code, and characterize the timing between the outputs. Also, computer glitches and I/O lag time can cause inconsistencies in time synchronization.

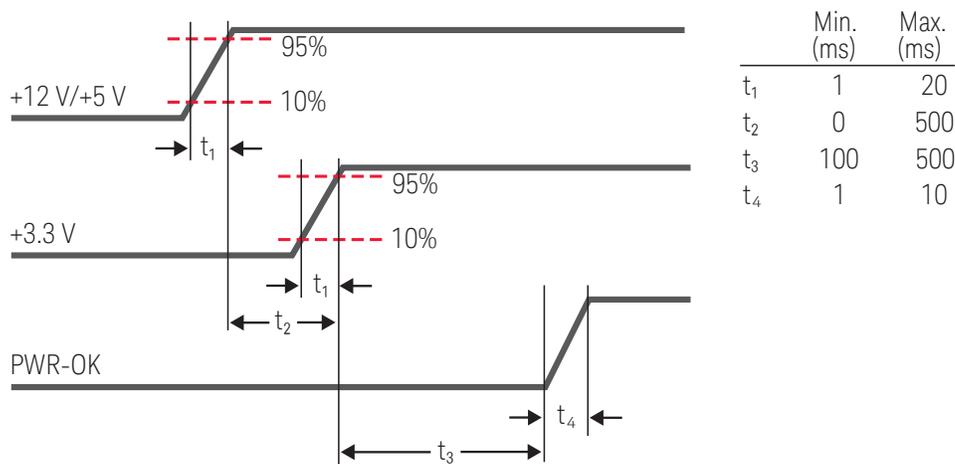


Figure 1. An example of PC motherboard power-on timing requirements

Solution: The Keysight N6705A DC Power Analyzer

The Keysight Technologies, Inc. N6705A DC Power Analyzer can precisely and repeatedly synchronize outputs. Designed for the bench, the DC Power Analyzer is an integrated instrument that has the capabilities of up to four power supplies, a function generator, an oscilloscope, a voltmeter, an ammeter and a datalogger in a single package. Intended for the R&D engineer, its intuitive design makes it quick to setup directly from the front panel. Over twenty different power supply modules can be mixed and matched to meet testing needs.

The 20+ different power modules have various voltage and current combinations organized in three different performance levels: basic performance, high-performance and precision. The high-performance and precision power modules, N675x and N676x respectively, have the fast programming times and accuracy specifications favored in this application. These modules have built-in output delay controls that can be configured directly from the front panel without the need to write a program.

Output sequencing

The N6705A has built-in output delay controls that are accessible from the front panel. As shown in Figure 2a, users can program output on and off delays by setting the time delay before the output turns on and off. This delay is applied after the All Outputs On or Off key is pressed. Users can enter delays from 0 ms to 1023 ms in 1 ms increments.

The example described in Figure 1 requires the 3.3 V voltage line to turn on at a time delayed by t_2 after the 12 V and 5 V voltage lines turn on. Here, the output on delays are referenced to the channel 1 voltage turning on. Figure 2b displays one example of a power-on sequence. The turn-on times reflect the delays set in Figure 2a.

Note that the N6705A can also measure the current being drawn from the power modules and display it in scope view or by using the datalogger.

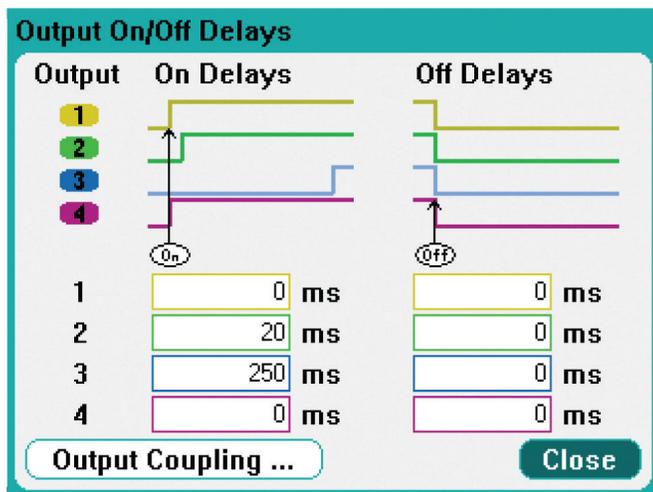


Figure 2a. Screenshot of output delay controls on N6705A.

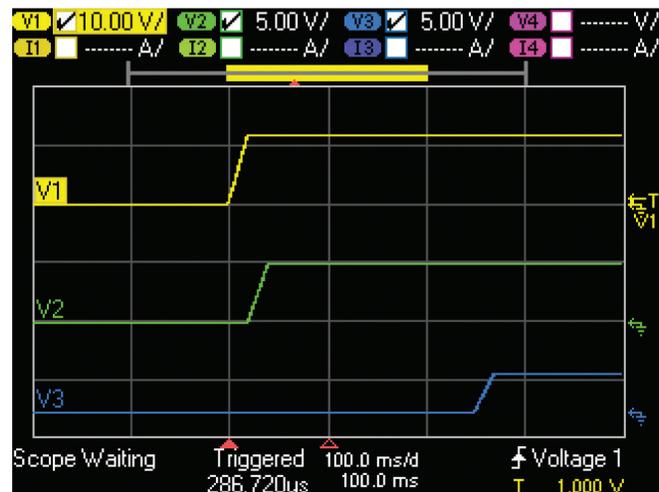


Figure 2b. Screenshot of scope view on N6705A.

Programmable slew rates

In addition to having multiple programmable output delays, the N6705A has programmable slew rates which control the voltage rate of change. Slew rates can be programmed as slow as 4.76 V/s. The maximum slew rate is limited by the up and down programming time of the module and the load created by the DUT. As a best case scenario, the N6751A and N6752A modules have a maximum up-programming slew rate of 50 kV/s with a full resistive load (10% to 90% of total voltage). Each module has its own controllable slew rate.

Fit for the bench

Designed with the R&D and design validation engineer in mind, users can configure all functions intuitively from the front panel. Users can set output voltages, current limits and output delays by navigating through output channel settings. Users can also setup data traces and output triggers through scope view properties. All of this can be done without the need for a computer, programming environment and many lines of code.

While the N6705A is optimized for use on the bench, it is also LXI class C compliant with LAN, GPIB and USB interfaces. The N6705A can be controlled from standard interfaces or through the built-in web server in addition to the control from the front panel.

For similar applications in manufacturing and ATE, consider the system version of the DC Power Analyzer, the 1.75 inch high N6700 Low-Profile Modular Power System. The N6700 has the same modular concept of the N6705A and uses the same power modules.

Summary

With the Keysight N6705A DC Power Analyzer, users can quickly and easily synchronize the power outputs required in multiple bias applications such as powering PC motherboards. It is an intuitive, flexible solution for the R&D or design validation engineer for a number of power applications.

Related applications

- Biasing power amplifiers
- Sequencing power rails
- Powering ICs

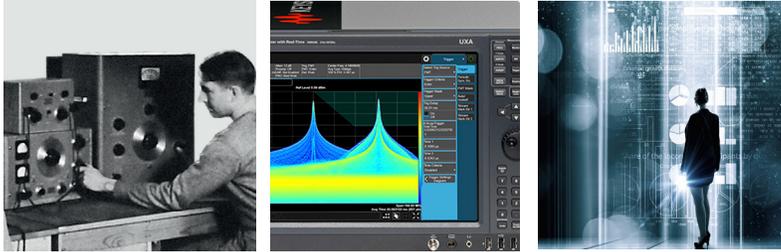
Related products

- N6700 Low-Profile Modular Power System

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